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In addition to the optical and electrical characterization we have attempted to assess the diode reliability by performing repetitive reverse IV measurements up to 1 V. We observed a random and sudden breakdown in some of these devices after repeated operation; this takes the form of an abrupt switch in the IV characteristics from diode like behavior to a short circuit. At present the operation lifetime before this breakdown occurs appears to be random. Currently we cannot ascertain the origin of this failure mode since this behavior has not been observed in the reference etched device nor has any change been observed in the resistivity of the implanted layers used for the TLM analysis.

#### **4. Conclusions**

This work has shown that it is possible to use He implantation with InAs to produce highly resistive areas and that when combined with post implantation annealing, a sufficiently high resistive region can be formed to allow the fabrication of a planar photodiode. Due to the implantation conditions used in this work an isolation depth of only around 2 μm has been achieved despite the total device thickness being 8 μm. As such this appears to have limited the upper voltage limit that the diodes can be operated to due to the onset of edge breakdown.

We have measured similar external quantum efficiencies for an implanted and subsequently annealed device as we would obtain for a reference etched device. These results also suggest that it may be possible to use implantation techniques to fabricate photodiodes in narrower bandgap materials such as InSb to allow the realization of long wavelength planar detectors.

In summary we have performed He ion implantation into InAs and performed post implant annealing. From TLM measurements we have shown that the sheet resistance of InAs can be dramatically increased. By also performing implantation on p-i-n structures we have shown that it is possible to produce planar photodiodes with comparable efficiencies to reference etched diodes.

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